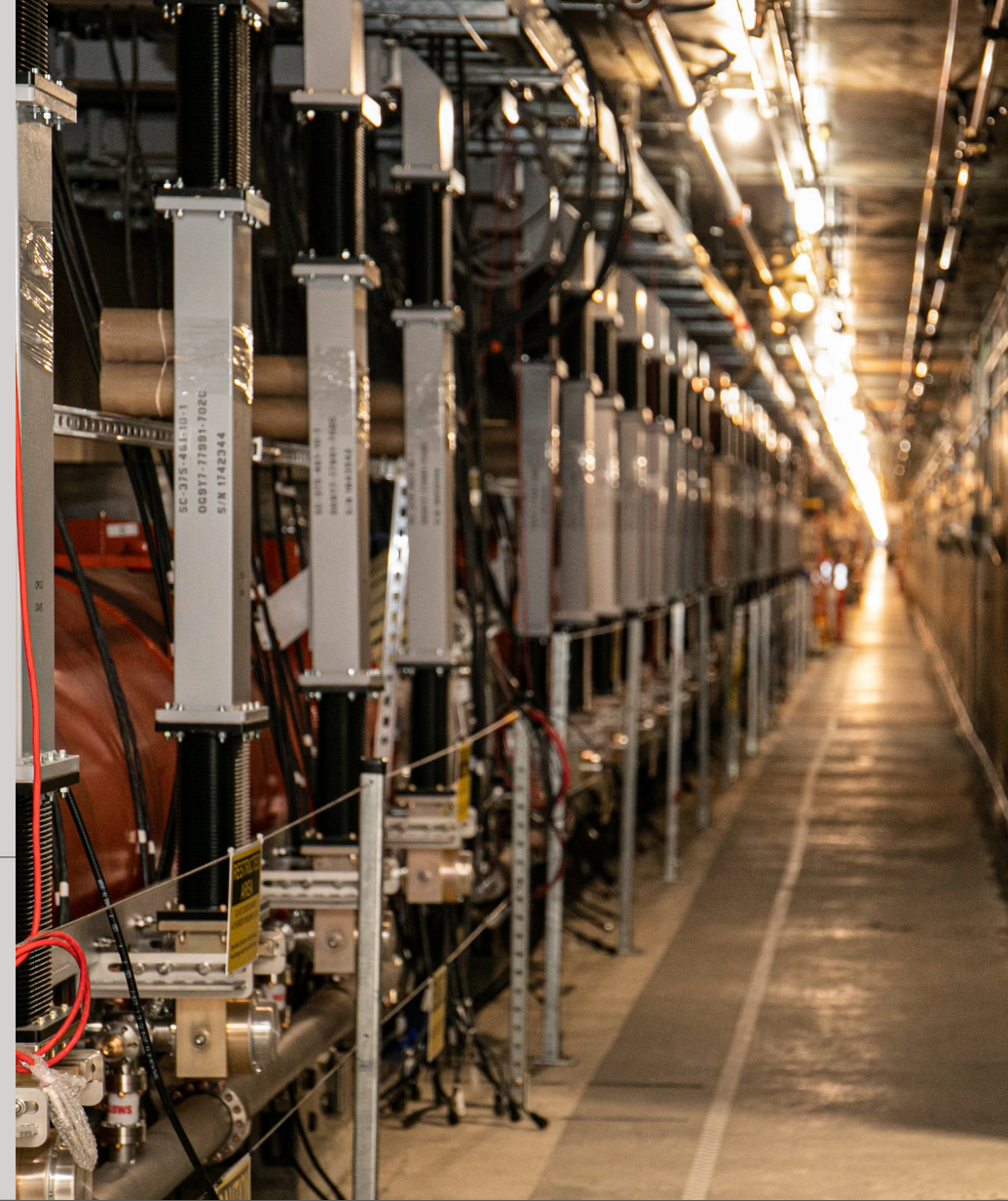


Commissioning of the LCLS-II Machine Protection System for MHz CW Beams

TU3I01

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12 September 2023



What is a Machine Protection System at SLAC

- The Machine Protection System (MPS) protects components in the accelerator from damage caused by the electron beam
- Types of damage:
 - Accumulation of radiation damage over time due to partial beam loss
 - Material melting and/or ablation by direct electron or x-ray beam impact
- MPS works to disable the electron beam.
 - Rate reduction (power reduction)
 - Complete shutoff of photocurrent
- The MPS is ***NOT*** a credited safety system. It is designed to protect the investment

At SLAC, the Beam Containment System is designed to protect people from the accelerator, and the MPS is designed to protect the accelerator from people

Each *accelerator* has its own MPS

SLAC Linear Accelerator Facility

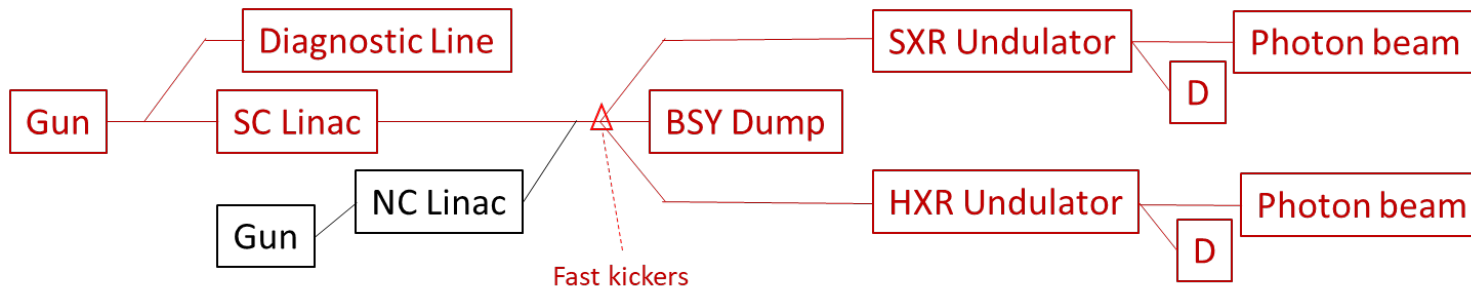


SLAC Linear Accelerator Facility consists of the LCLS complex and the FACET-II accelerator

LCLS Complex consists of LCLS and LCLS-II Accelerators, 2 undulator beamlines to produce FEL, multiple experimental end stations, and a switching scheme in the middle to facilitate delivery of either accelerator to either undulator beamline

The LCLS Complex and FACET-II each have their own Machine Protection Systems

MPS in the LCLS Complex



LCLS-II MPS needs to respond to avoid damage to critical accelerator components such as superconducting cavities, beamlines, undulator permanent magnets, photon optics, etc.

- LCLS-I: MPS responded to a fault by the next bunch (~8 ms)
- LCLS-II: MPS required to respond within 100 μ s to avoid catastrophic damage

LCLS-II Upgrade

- Original LCLS was 1 source to 1 user, pulsed RF, 120 Hz duty cycle, fixed gap undulator system for FEL
- LCLS-II project installed:
 1. New accelerator with superconducting RF cavities capable of CW RF and 1 MHz duty cycle
 2. 2 variable gap undulator beamlines (soft x-rays and hard x-rays) to support 2 FEL simultaneously
- SC accelerator produces 4 GeV electrons at maximum rate of 1 MHz for maximum power of 120 kW

LCLS SC MPS Requirements

System requirements

- Must be compatible with different beam rates, from low-rate startup to full 1 MHz CW operation
 - Includes complex burst patterns
 - MPS must be aware of beam power levels
- MPS must lower the beam rate or shut off the beam completely according to fault severity or device insertion
- Rate should recover automatically when fault conditions clear (if the fault opts-in to automatic recovery)
- Must support different destinations for each pulse to allow interleaved delivery to multiple users.
- Must operate in parallel with LCLS-I
- Time between fault occurrence and beam shutdown must be less than 100 μ s for “fast” faults.
 - Must also remain as low as reasonably achievable (ALARA)
 - Standard faults will follow LCLS-I’s requirements: shutdown within 2.3 ms.



Mitigation Strategy

MPS uses two mitigation paths to inhibit the beam

1. Rate reduction

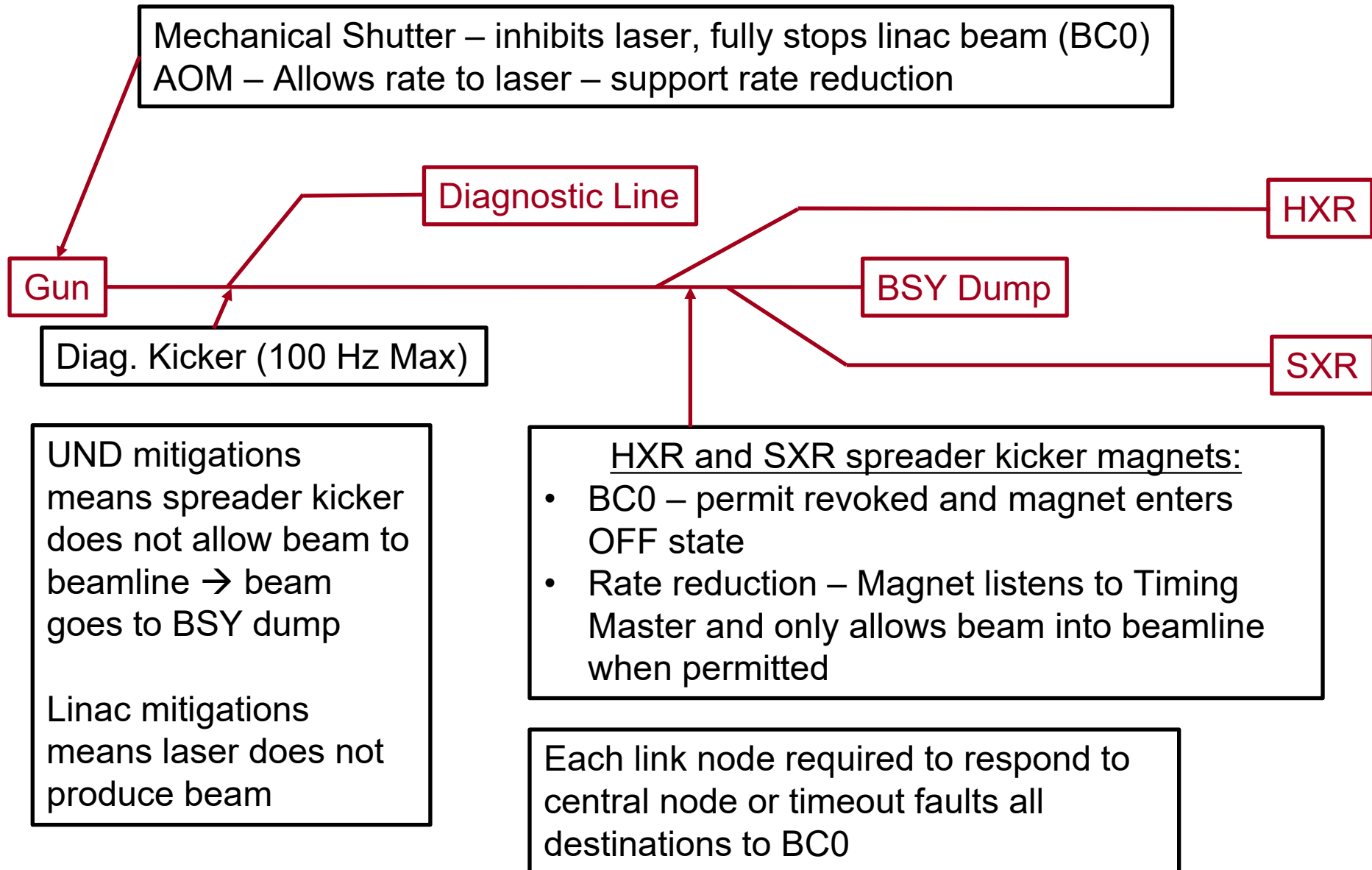
- The MPS imposes a Beam Class. A Beam class is defined as a **maximum integrated charge** in a defined **time interval** with some **fixed beam spacing (rate)**
- The timing master has a series of pre-programmed “Allow Tables” that correspond to each beam class. The allow table is combined with the user request table to define the actual beam rate. **See TH1101 from Carolina Bianchini Mattison on Thursday for more about the LCLS-II timing system**
- Mitigation Devices listen to these triggers to fire, and MPS verifies they have fired when expected. If a mismatch, go to beam off condition

2. Beam Shut Off

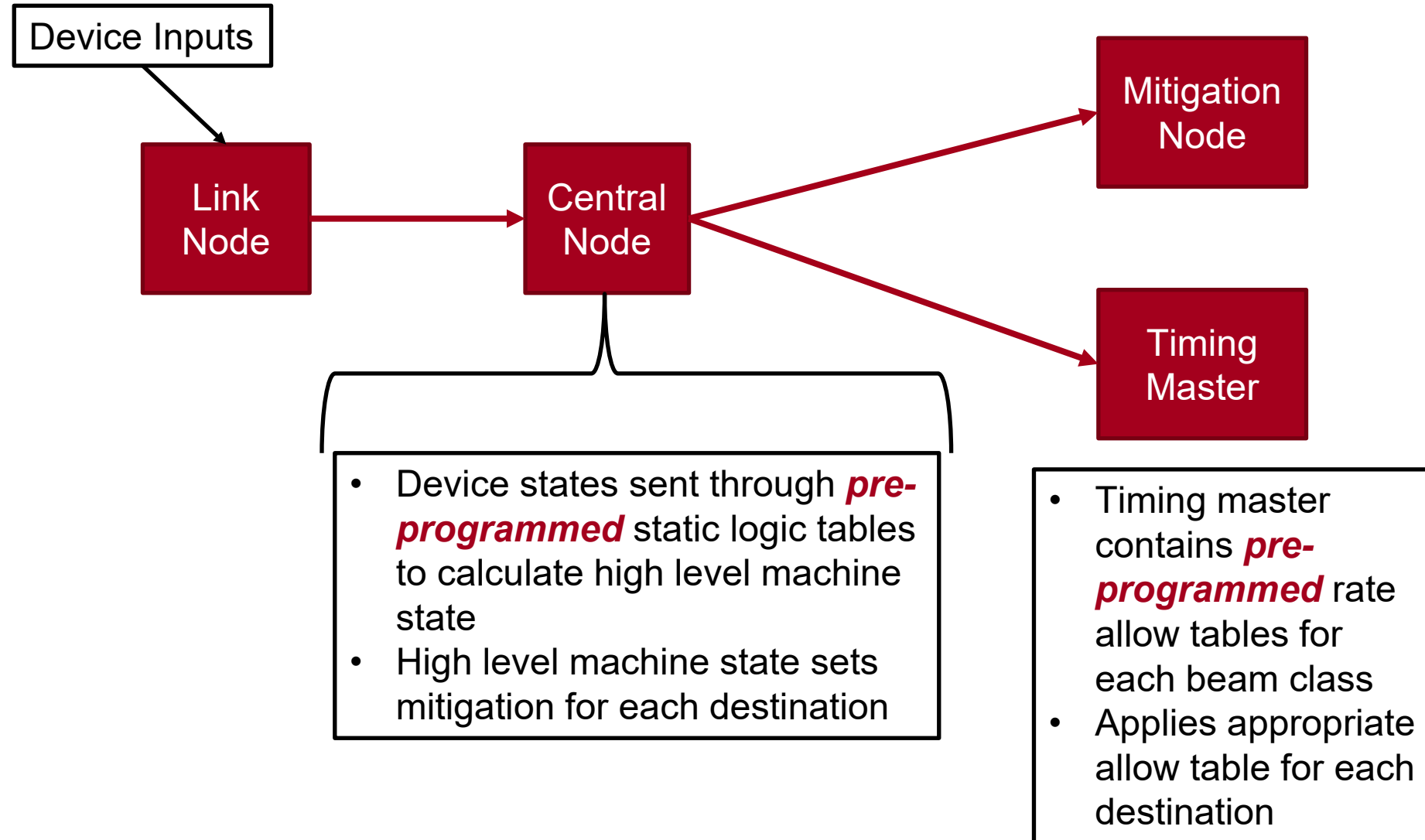
- Beam Class 0 is defined as no beam allowed
- MPS has permit signals to each mitigation device that are revoked when in beam class 0

MPS permits are *per destination*, so each destination has a mitigation device to allow beam into that zone

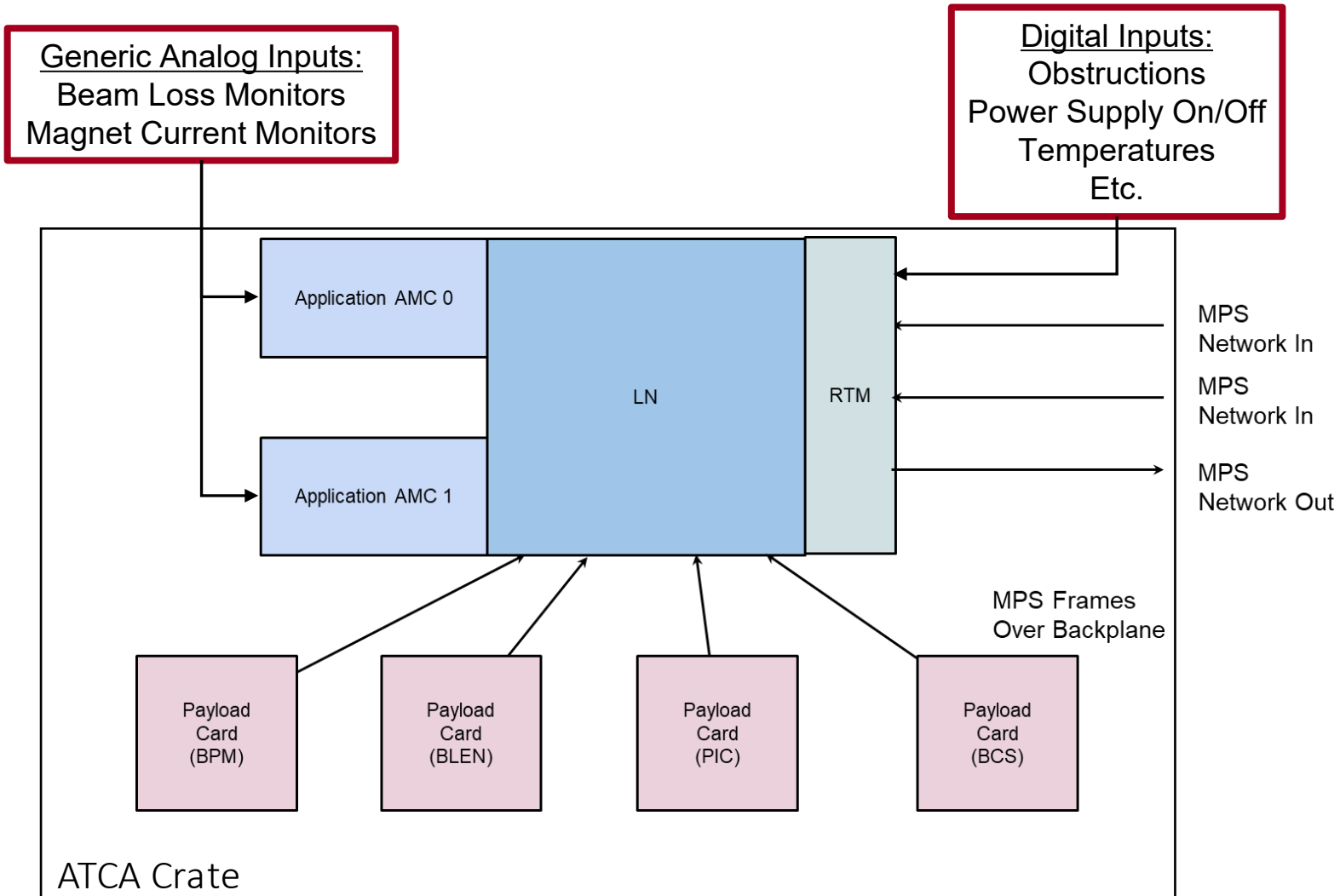
MPS Mitigation Devices



LCLS SC MPS High Level Architecture



LCLS SC MPS Link Node



Link Node is a data collector

- Built upon SLAC common platform ATCA architecture
 - Every ATCA crate has 1 link node and multiple other application payloads
- Signals enter application
 - Thresholds applied if applicable (analog signals)
 - Message constructed and sent to link node
- Link node collects messages, aggregates them, and resends them to next node in the chain
- Final link node sends complete aggregate to central node



LCLS SC MPS Inputs

Type	Number
Beam Position Monitor	302
Beam Loss Monitor	276
Bunch Charge Monitor	7
Magnet Current	84
Photon MPS	62
Obstruction	636
Total	1367

Pre-emptive: limit beam power if beam trajectory deviates from nominal

React to loss of beam

Primary verification of power levels and destination

Ensure bend magnets are set properly for beam transport

Photon systems manage end station MPS for agility

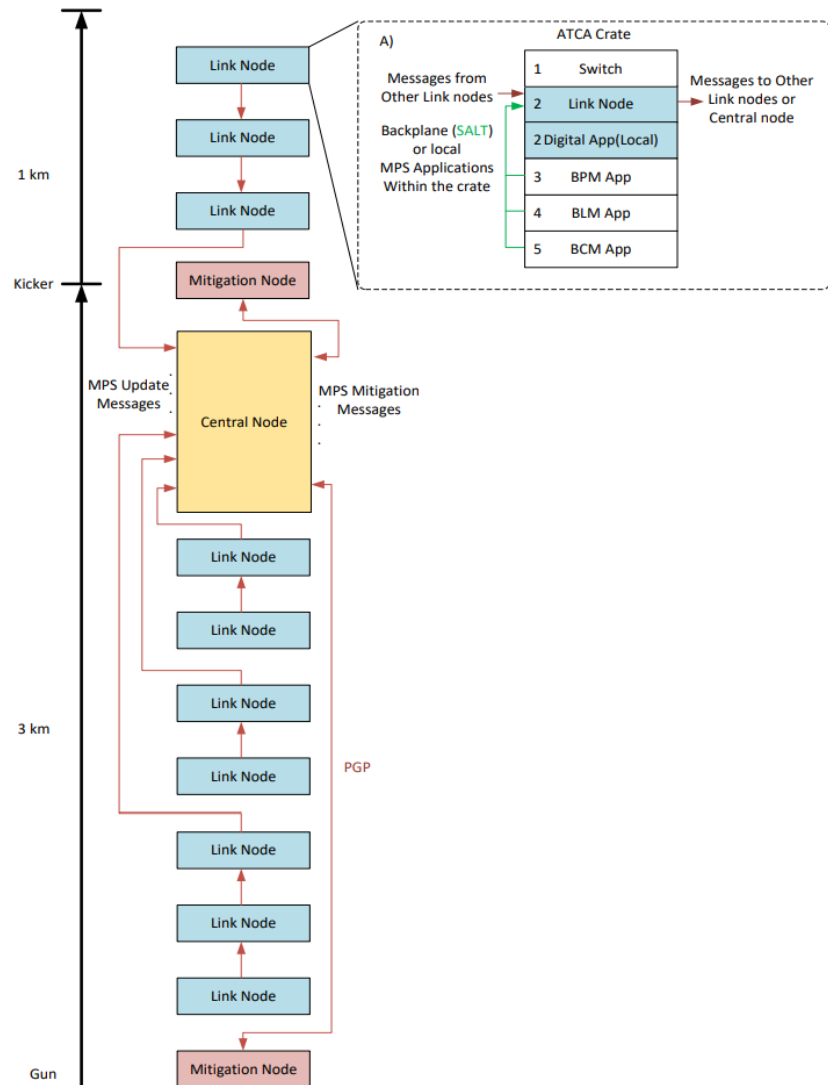
Reactive to beamline obstructions



Point beam loss monitor at a linac collimator

See Alan Fisher and TUP005 this evening for a description of the LCLS-II Beam Loss Monitor System

LCLS SC Low Latency Network

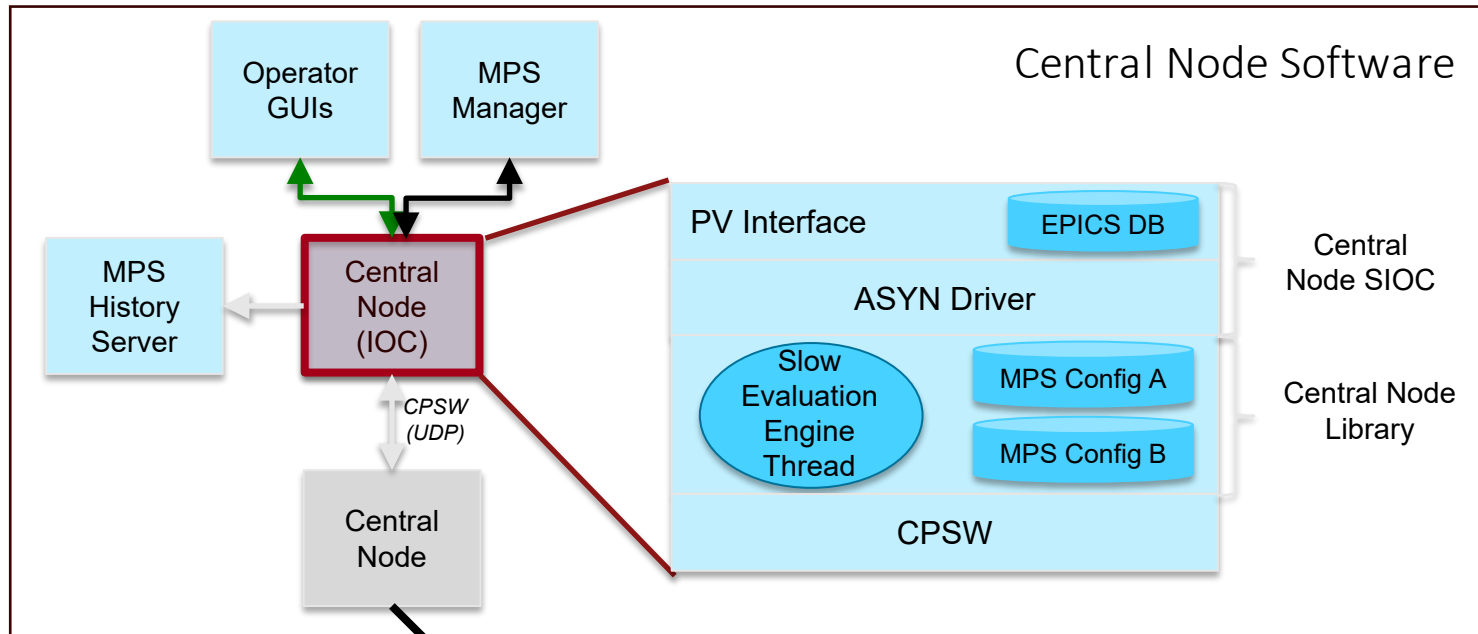


- Link nodes are arranged into groups, chained from one to the next
- Communication is one direction and event driven – each accelerator clock cycle (1 us) link nodes send out a message
- Bandwidth of each group is 5 Gbps
 - 25 link node groups organized so group bandwidth < 2.5 Gbps → room for future infill if needed
- Central node collects data from up to 12 link node groups

MPS central nodes



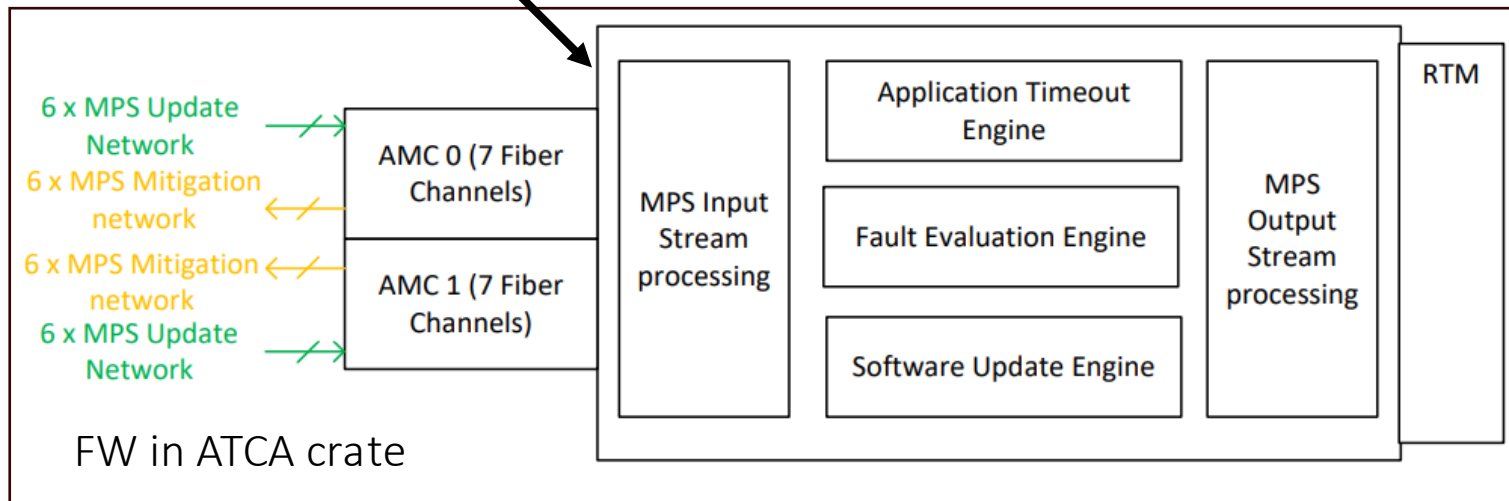
LCLS SC MPS Central Node



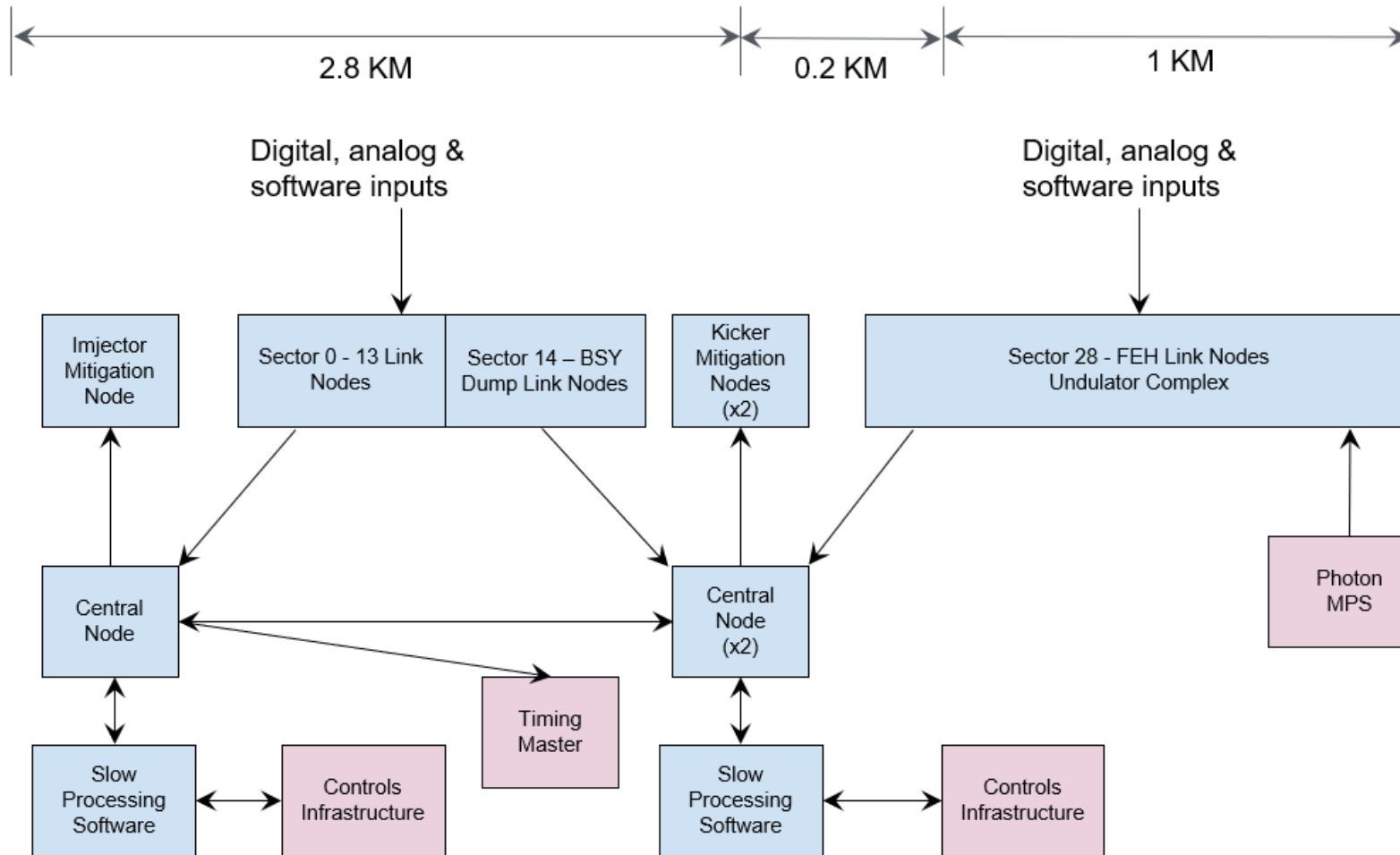
Central Node monitors:

1. Fast rules evaluation
 - Single bit decisions are made in FW rules table in 1 us
2. Application timeout
 - Each discreet application must send a new message
3. Slow rules evaluation
 - Information streamed to computer at 360 Hz for evaluation of complex rules
4. Timing pattern
 - Timing system information is monitored for compliance

Final permit is the result of the combination of *all* the above



System Implementation



To further manage latency and bandwidth, three central nodes are installed

Each central node operates independently

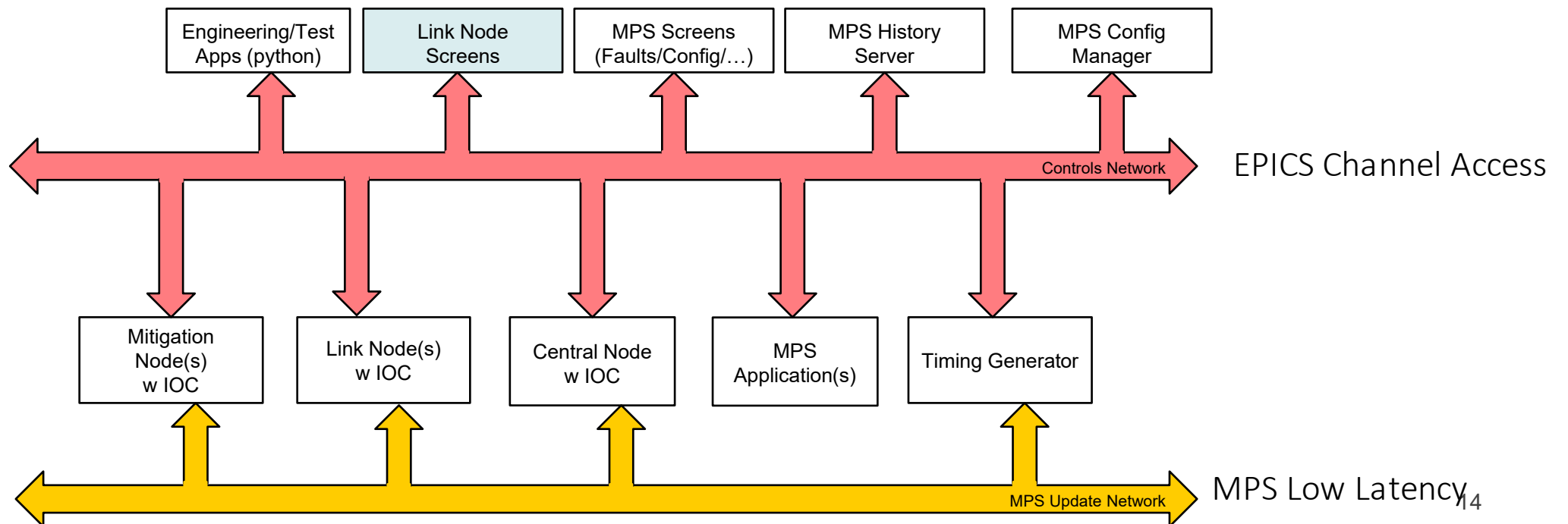
The three central nodes exchange permit data after each cycle (1 us)

The final permit each reports is the lowest of any of the three

Software Support

MPS Software built against the EPICS toolkit

- SLAC common platform has an industrial CPU attached to each ATCA crate
- Kernel driver support provides a mechanism to attach one EPICS IOC to each payload in the crate
- EPICS IOC for link node and central node provides asynchronous and some beam synchronous data for consumption
- EPICS channel access is used to send and receive information from operations into the hardware



Software Tools

The screenshot shows the LCLSHome interface with the following details:

- Source:** NC (radio button), SC (radio button)
- Destinations:** DIAGO, BSYPDump, HXR, SXR
- EDM Area Main...** button
- Home View** tab selected, with sub-tabs: LOBAI, GUNB, L0B, L1B, L2B, L3B, EXT, DOG, BYP, SPR, DASEL, BSYO, LTUH, LTUS, UNDH, UNDS, DMPH, DMPS, FEEH, FEES, NEH1, XRT1, FEH1
- Permit Status** section:

	SC_BSYD	SC_DIAGO	SC_HXR	SC_SXR	SC_LESA	LASER_HTR	LASER
Allowed:	1% MAP	Beam Off	1% MAP	Beam Off	Beam Off	1% MAP	Full
Beam Permit:	Allow Beam	Allow Beam	Allow Beam	Allow Beam	Allow Beam	Allow Beam	
	Revoke Permit	Revoke Permit	Revoke Permit	Revoke Permit	Revoke Permit	Revoke Permit	
Requested BC:	1% MAP	BC120Hz	1% MAP	1% MAP	Beam Off		
Shutter:	OUT	BKRDGO	BKYSPH	BKYSPS	DASEL	LHS: OUT	Attn: OUT
AOM:							
- Control** section:
 - Reset All
 - Unlatch All
 - Central Node Core...
 - Mitigation Devices...
 - Speed Limit: 1% MAP
 - AOM State Machine... Accelerate
- Status** section:
 - Link Nodes...
 - MPS Groups...
 - Application Cards...
 - Timing Pattern Check
 - All Inputs...
- Tools / Displays** section:
 - SCMPS Gui / Logic...
 - BLM Display...
 - Beam Class Defs...

High level permit and status

Example low-latency group

The screenshot shows the SC Linac MPS Link Node Group 22 interface with the following details:

- SC Linac MPS Link Node Group 22** (Title)
- LN143** (shm-undh-sp03-1):

Slot	AID	MPS On	Type	Details
RTM	374	Enabled	MPS_DI	More Info...
2	330	Enabled	MPS_AI	More Info...
3	331	Enabled	BPM	More Info...
4	332	Enabled	BPM	More Info...
5	333	Enabled	BPM	More Info...
6	334	Enabled	BPM	More Info...
7	---- Slot not used in MPS ----			
- LN144** (shm-undh-sp03-2):

Slot	AID	MPS On	Type	Details
RTM	375	Enabled	MPS_DI	More Info...
2	335	Enabled	MPS_AI	More Info...
3	336	Enabled	BPM	More Info...
4	337	Enabled	BPM	More Info...
5	338	Enabled	BPM	More Info...
6	339	Enabled	BPM	More Info...
7	---- Slot not used in MPS ----			
- LN141** (shm-undh-sp02-2):

Slot	AID	MPS On	Type	Details
RTM	316	Enabled	MPS_DI	More Info...
2	317	Enabled	MPS_AI	More Info...
3	214	Enabled	MPS_AI	More Info...
4	318	Enabled	BPM	More Info...
5	319	Enabled	BPM	More Info...
6	320	Enabled	MPS_AI	More Info...
7	---- Slot not used in MPS ----			
- CN B005-S3** (Rx Port 8):

Slot	AID	MPS On	Type	Details
RTM	316	Enabled	MPS_DI	More Info...
2	317	Enabled	MPS_AI	More Info...
3	214	Enabled	MPS_AI	More Info...
4	318	Enabled	BPM	More Info...
5	319	Enabled	BPM	More Info...
6	320	Enabled	MPS_AI	More Info...
7	---- Slot not used in MPS ----			
- Data Flow:**
 - LN143 (PGP Rx) → LN144 (PGP Tx): 534.9 Mbps
 - LN144 (PGP Rx) → LN141 (PGP Tx): 1069.8 Mbps
 - LN141 (PGP Rx) → CN B005-S3 (PGP Tx): 1604.6 Mbps
- Buttons:** Link Node..., Inputs..., Faults..., Clear Application Timeouts

Software Tools

SC MPS GUI - PyDM@lcls-srv01

File View History Tools

SC_BSYD
MPS Permit

1% MAP

Timing BC
1% MAP

Timing Rate
100 Hz

SC_DIAG0
MPS Permit

Beam Off

Timing BC
Beam Off

Timing Rate
0 Hz

SC_HXR
MPS Permit

1% MAP

Timing BC
1% MAP

Timing Rate
1000 Hz

SC_SXR
MPS Permit

Beam Off

Timing BC
Beam Off

Timing Rate
0 Hz

LASER
MPS Permit

Full

Timing BC
Beam Off

Timing Rate
0 Hz

SC_LESA
MPS Permit

Beam Off

Timing BC
Beam Off

Timing Rate
0 Hz

Faulted Devices

Fault	State	SC_BSYD	SC_DIAG0	SC_HXR	SC_SXR	LASER	SC_LESA	LASER_HTR	MECH_SHUTTER
PRDAS12 (PROF:DASEL:440) Position	Moving	-	-	-	-	-	Beam Off	-	-
PRDAS14 (PROF:DASEL:655) Position	Moving	-	-	-	-	-	Beam Off	-	-
PRDAS17 (PROF:DASEL:818) Position	Moving	-	-	-	-	-	Beam Off	-	-
VVPG DASEL 559 Position	Is Faulted	-	-	-	-	-	Beam Off	-	-
VVPG DASEL 898 Position	Is Faulted	-	-	-	-	-	Beam Off	-	-
VVPG DASEL 244 Position	Is Not Open	-	-	-	-	-	Beam Off	-	-
VVFS DASEL 953 Position	Is Not Open	-	-	-	-	-	Beam Off	-	-
PMPS: SXR Fast Fault 1 (KFE)	Is Not Ok	-	-	-	Beam Off	-	-	-	-
PMPS: SXR Motion Fast Fault 1 (KFE)	Is Not Ok	-	-	-	Beam Off	-	-	-	-
PMPS: SXR Rate Limit / Arbiter	SXR BC 8	-	-	-	Tuning	-	-	-	-

Check Engine Lights

- MP01
- MP02
- MP03

Bypassed Faults

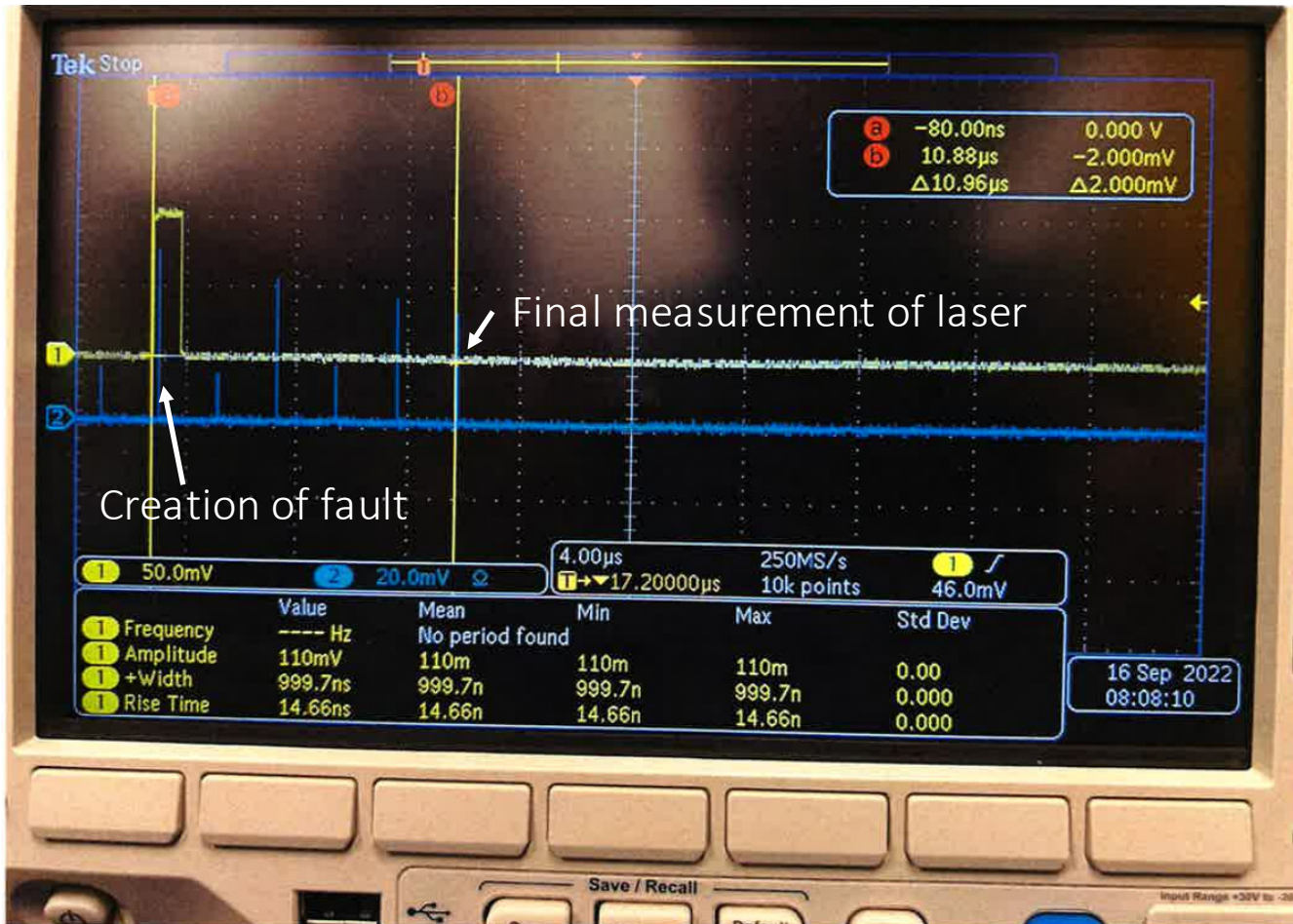
Fault	State	Bypass Exp Date
BPMDOG8 (BPMS:DOG:230) X Orbit Fault	-	Bypass date not set
BPMDOG8 (BPMS:DOG:230) Y Orbit Fault	-	Bypass date not set
SXRSS SLSXS1 (SLIT:UNDS:3555) Position	Out	Mon Dec 25 14:37:02 2023
SXRSS BOD10 (YAGS:UNDS:3575) Position	Out	Mon Dec 25 14:37:33 2023
PMPS: SXR TMO Motion Fast Fault 1 (TMO)	Is Ok	Mon Sep 11 15:05:12 2023

Summary Logic Ignore Logic App Status Configure

LCLS MACHINE PROTECTION SYSTEM

09/08/2023 16:16:38

Measurement of shutoff times



Use a signal generator to drive an analog fault in a link node (yellow trace)

Measure the laser pulses at 500 kHz (blue)

Time between start of fault and last blue spike is the shutoff time

Repeat for the kicker magnets

Kicker magnet shutoff times are acceptable for commissioning but not higher power modes. More work needs to be done to understand where the slowdown comes from

Destination	Maximum Commissioning Rate	Minimum Commissioning shutoff time	Measured shutoff time
Linac high power dump	93 kHz	100 us	12 us
HXR Undulator Beamline	1 kHz	1 ms	70 us
SXR Undulator Beamline	1 kHz	1 ms	110 us

Commissioning Experience

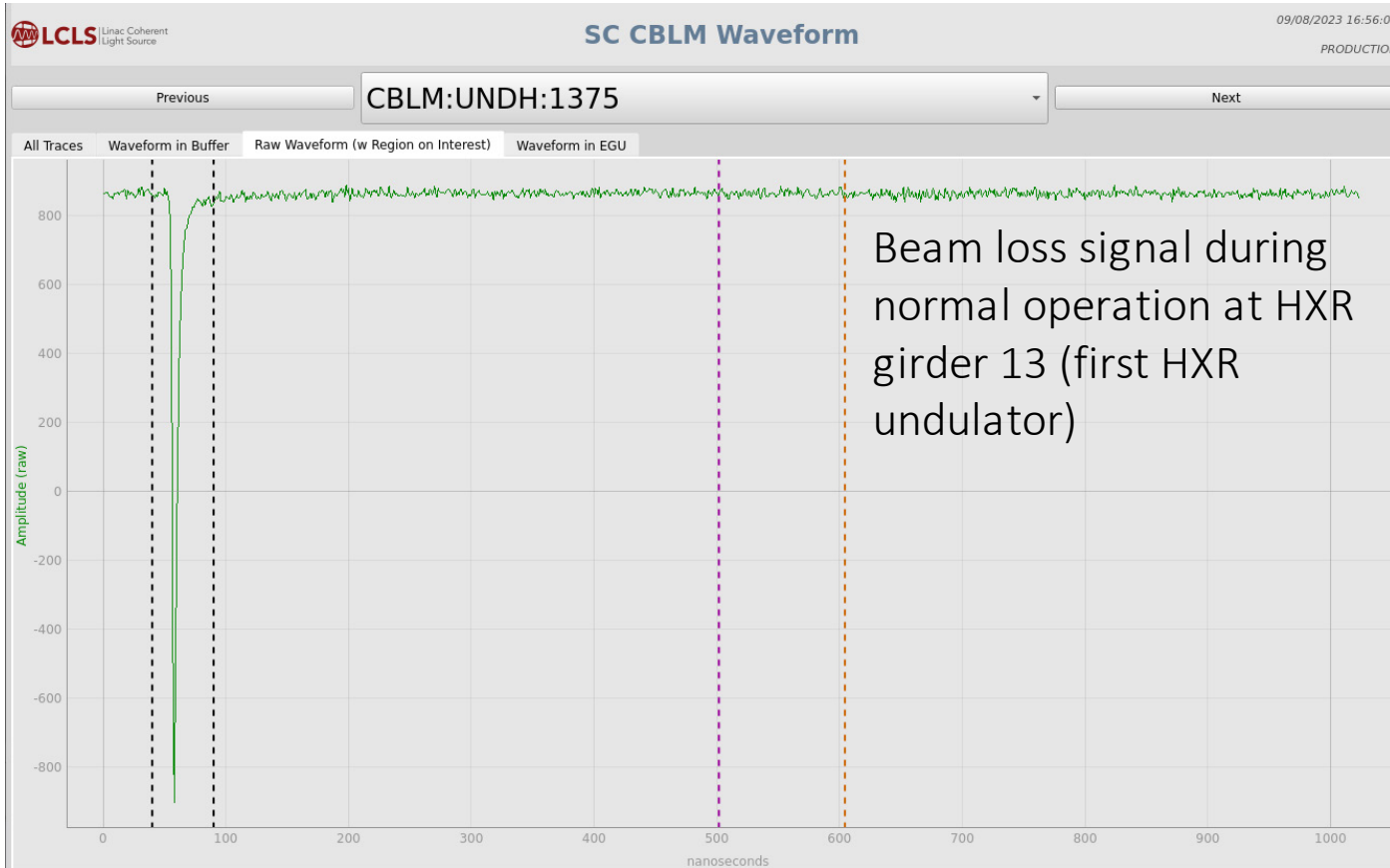
System designed with commissioning in mind

Commissioning happened in 3 phases:

1. Injector
 - Requires one central node
2. Linac
 - Requires two central nodes
3. Undulator Complex
 - Requires all central nodes

Goal is for MPS to stay out of the way unless it needs to intervene. When it intervenes, it must be able to tell operations why

Undulator Beam Loss Monitors



50 ns “beam window”

100 ns “pedestal window”

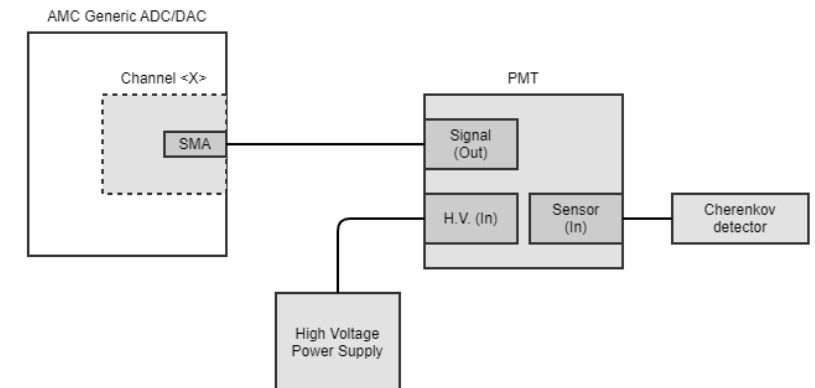
Processed “beam loss” is the average value in the beam window minus the average value in the pedestal window

Every undulator girder has a beam loss monitor attached

- 34 Hard Undulators
- 22 Soft Undulators

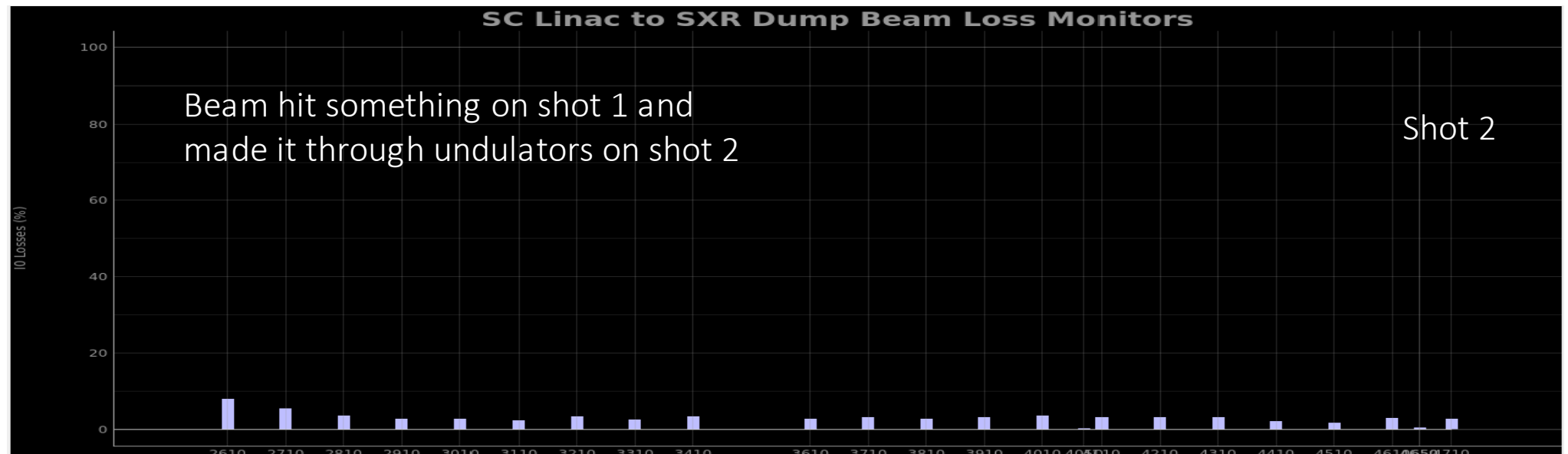
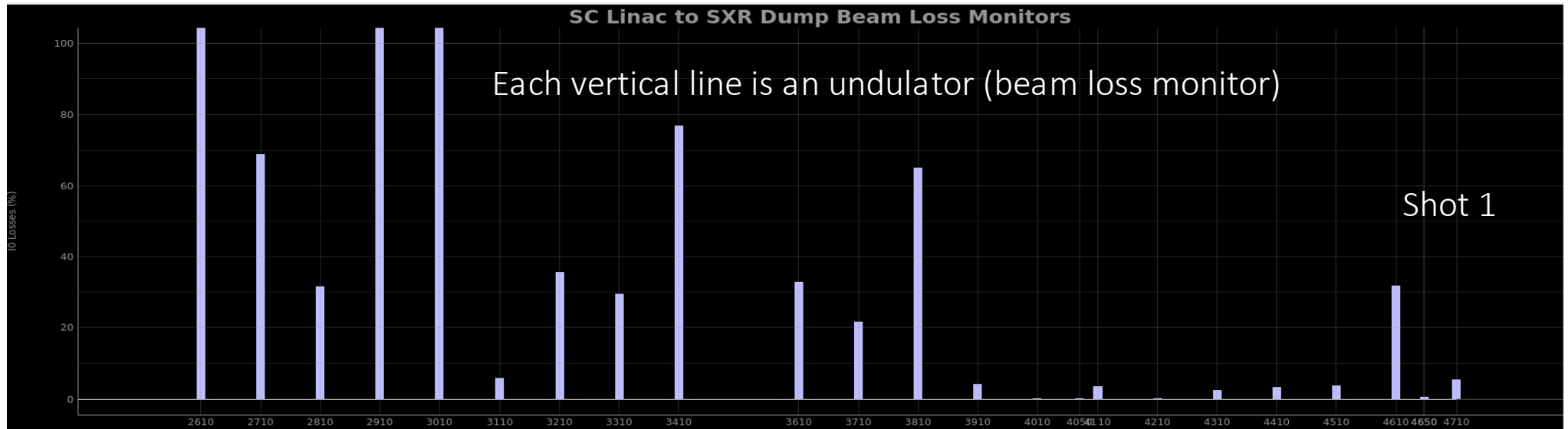
Cherenkov detector with a PMT mounted
375 MHz digitizer in link node collects and processes PMT data at full machine rate (1MHz)

“Beam window” needs to be found for each beam loss monitor

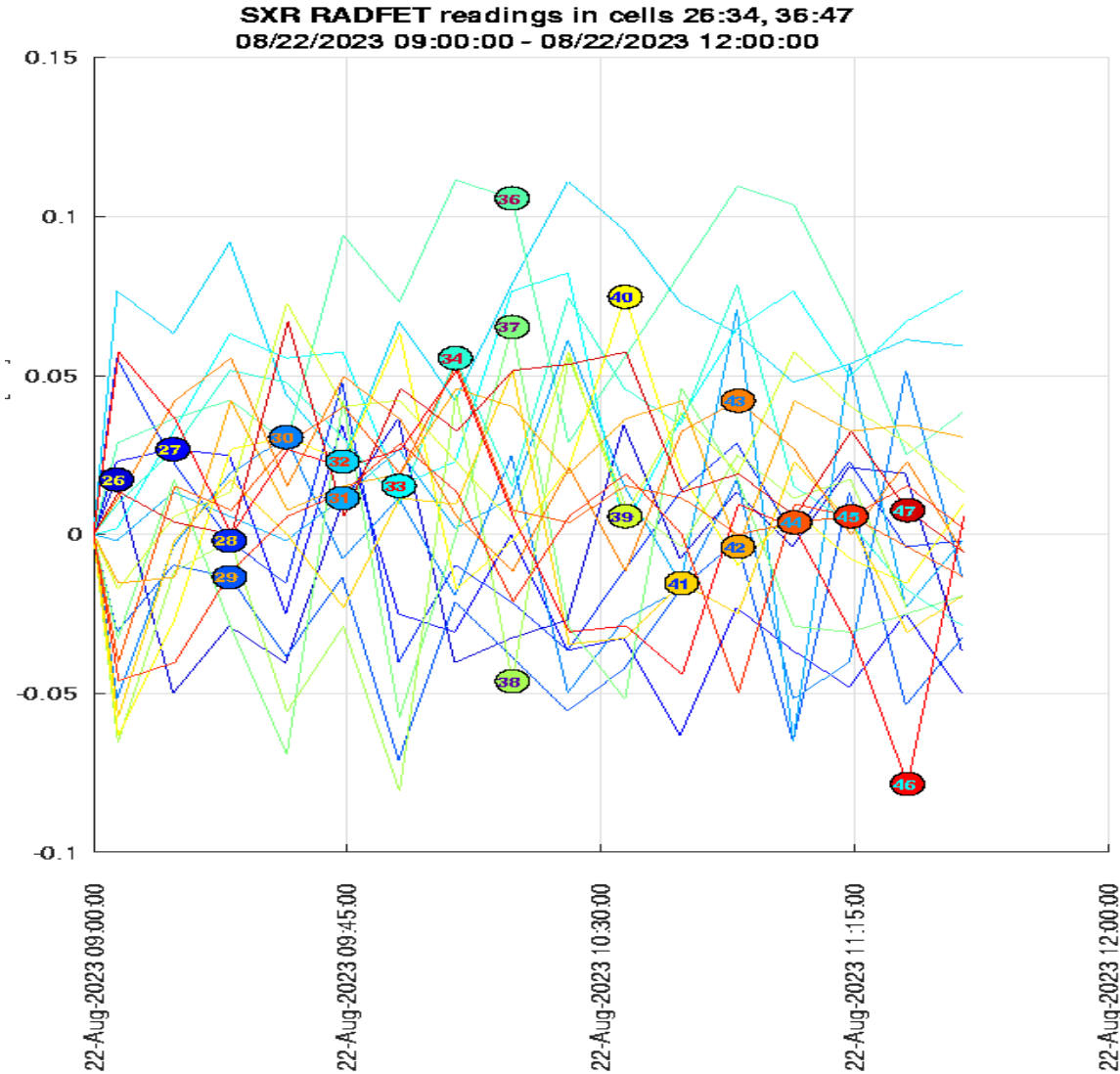


Utility of Undulator Beam Loss Monitors

Y axis is percent of trip threshold of processed beam loss value



RADFETs in undulator hall



Undulator girders have RADFETs

RADFETs not used for MPS

There as a diagnostic.

From the same time period as the previous plots, no significant accumulated dose observed at the RADFETs

Indicates the MPS beam loss monitors are not failing

General Beam Loss Monitors



MPS and BCS Share

General beam loss monitoring scheme at LCLS complex described by Alan Fisher at [TUP005](#)

String of beam loss monitors that cover the entire 4 km extent of LCLS-II, instrumented as part of the Beam Containment System (safety system)

BCS processes the data and sends MPS the result as an analog signal → MPS can set tighter limits and trip first with the same beam loss signals

Slow waveform view of general beam loss monitors as well. Can be used for beam loss localization

BPM Directionality

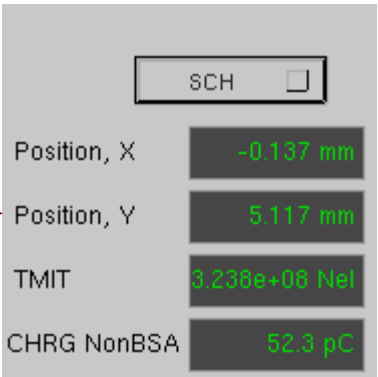
BPM orbit interlocks are used to pre-emptively reduce beam power when trajectory deviates from nominal

Certain BPMs near kicker magnets can detect beam straight ahead or kicked. Kicked beam shows up as a deviation from nominal

MPS is event driven and timing stream determines the beam destination, so thresholding is dependent upon destination

Kicked into HXR beamline


Kicked beam
Y required to be within
3 mm to 7 mm



Parameter	Value
Position, X	-0.137 mm
Position, Y	5.117 mm
TMIT	3.238e+08 Nel
CHRG NonBSA	52.3 pC

Straight ahead into BSY Dump

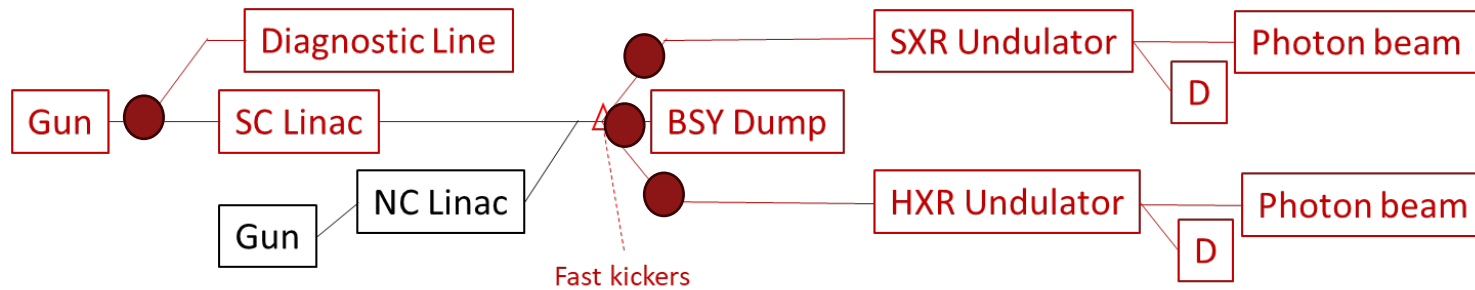
Straight ahead beam
Y required to be within
-2 mm to 2mm



Parameter	Value
Position, X	-0.426 mm
Position, Y	0.333 mm
TMIT	3.152e+08 Nel
CHRG NonBSA	50.5 pC

The image shows two screenshots of BPM data. The left screenshot is for 'SCH' and shows a Y position of 5.117 mm. The right screenshot is for 'SCBSYD' and shows a Y position of 0.333 mm. Red arrows point from the text 'Kicked beam Y required to be within 3 mm to 7 mm' to the Y position value in the SCH screenshot, and from the text 'Straight ahead beam Y required to be within -2 mm to 2mm' to the Y position value in the SCBSYD screenshot.

Bunch Charge Monitor Interlock



Diagnostic toroids are installed in each beamline

Within SC linac, toroids installed in each warm beamline section

Bunch charge measurement feeds MPS interlock

Bunch Charge Monitors

- Diagnostic bunch charge monitor system measures bunch charge for each potential beam pulse
 - Either measures bunch or absence of bunch
- Timing system encodes destination information as part of the pattern
- MPS uses destination information to choose between 2 threshold tables
 1. Beam present
 2. No-beam
- Acts as a destination monitor and a measure of beam loss or power violation
 - Too little charge = loss
 - Too much charge = violation

Conclusions

- LCLS-II project at the SLAC Linear Accelerator Facility installed a new linear accelerator capable of producing 120 kW beams
- Precious components such as cryogenic RF cavities, beamlines, undulator permanent magnets, photon optics can be damaged by the electron beam
- LCLS SC MPS designed to keep people from damaging the accelerator
- LCLS SC MPS has been fully deployed and checked out. Commissioning underway
- LCLS SC MPS was designed to grow with the facility

Continuing work:

- Better understanding of shutoff time for the undulator complex needed
- MPS only affects photocurrent, but dark current can be a problem
 - Have observed that if some RF cavities in bad state MPS can be “tripped” but dark current can still be present
 - Considering a slow SW monitor to insert a stopper in the event of an MPS trip AND consistent beam loss